

ABSTRACT

The invention relates to a parallel inverter system, in which each inverter includes a synchronized square wave generator, a voltage given generator, a voltage regulating unit and a power amplifier unit. All of them are connected in parallel in turn. The characteristic is that the output current given of the voltage regulating unit is no longer selection of one out of many any more, but outputs the output of voltage regulators of all of voltage regulating units after linear combination, thereby changing the previous unequal parallel connection to equalized parallel connection. At the same time, the output square waves of all of synchronizing square wave generators are inputted into voltage given generators as the synchronizing square wave after AND. The output sine waves of all of voltage given generators after linear combination are used as given voltage of voltage regulating units.